

a second circuit for generating a negative polarity voltage;

a negative polarity voltage outputting terminal for outputting the negative polarity voltage from said second circuit;

Claim
a ground terminal for providing a reference potential for both of said positive polarity voltage and said negative polarity voltage; and

a short circuit for short-circuiting substantially between said positive polarity voltage outputting terminal and said negative polarity voltage outputting terminal in response to a power-off signal;

wherein residual charges of the capacitor pass said short circuit in turning a power off; and.

wherein said first circuit includes a chopper circuit for generating a low first positive voltage.

4.(Twice Amended) A power supply circuit according to claim 1, wherein said first circuit includes a chopper circuit for generating a low first positive voltage, and a fly-back circuit for receiving the first positive voltage from the chopper circuit to generate a high second positive voltage,

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said positive polarity voltage outputting terminal includes first and second output terminals for respectively outputting the first positive voltage and the second positive voltage, and further comprising:

a diode connected between said first and second output terminals in a forward direction from said first output terminal to said second output terminal.

5. (Amended) A power supply circuit, comprising:

a chopper circuit for generating a low first voltage;

a fly-back circuit for receiving the first voltage from said chopper circuit to generate a high second voltage;

first and second terminals for respectively outputting the first and second voltages as power outputs of the power supply circuit; and

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a diode connected between said first terminal and said second terminal in a forward direction from said first terminal to said second terminal.

6. (Amended) A camera, comprising:

a micro-computer;

a first circuit for generating a positive polarity voltage;

a first terminal for outputting the positive polarity voltage from said first circuit;

a second circuit for generating a negative polarity voltage;

a second terminal for outputting the negative polarity voltage from said second circuit;

a short circuit for short-circuiting substantially between said first terminal and said second terminal in response to a power-off signal from said micro-computer; and

a CCD imager for receiving the positive polarity voltage and negative polarity voltage through said first terminal and said second terminal.

7. (Twice Amended) A camera, comprising:

a chopper circuit for generating a low first voltage;

a fly-back circuit for receiving the first voltage from said chopper circuit to generate a high second voltage;

CH first and second terminals for respectively outputting the first voltage as a positive polarity voltage and the second voltage as a negative polarity voltage;

a diode connected between said first terminal and said second terminal in a forward direction from said first terminal to said second terminal; and

a CCD imager for receiving the positive polarity voltage and negative polarity voltage through said first terminal and said second terminal.

8. (Amended) A power supply circuit, comprising:

a first circuit for generating a positive polarity voltage;

a first terminal for outputting the positive polarity voltage from said first circuit;

a second circuit for generating a negative polarity voltage;

eb a second terminal for outputting the negative polarity voltage from said second circuit; and

CH a short circuit for short-circuiting substantially between said first terminal and said second terminal in response to a power-off signal;

wherein said first circuit includes a chopper circuit for generating a low first positive voltage, and a fly-back circuit for receiving the first positive voltage from the chopper circuit to generate a high second positive voltage, and further comprising:

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cont*
first and second output terminals for respectively outputting the first positive voltage and the second positive voltage as outputs of the power supply circuit; and
a diode connected between said first and second output terminals in a forward direction of from said first positive voltage output terminal to said second positive voltage output terminal.
